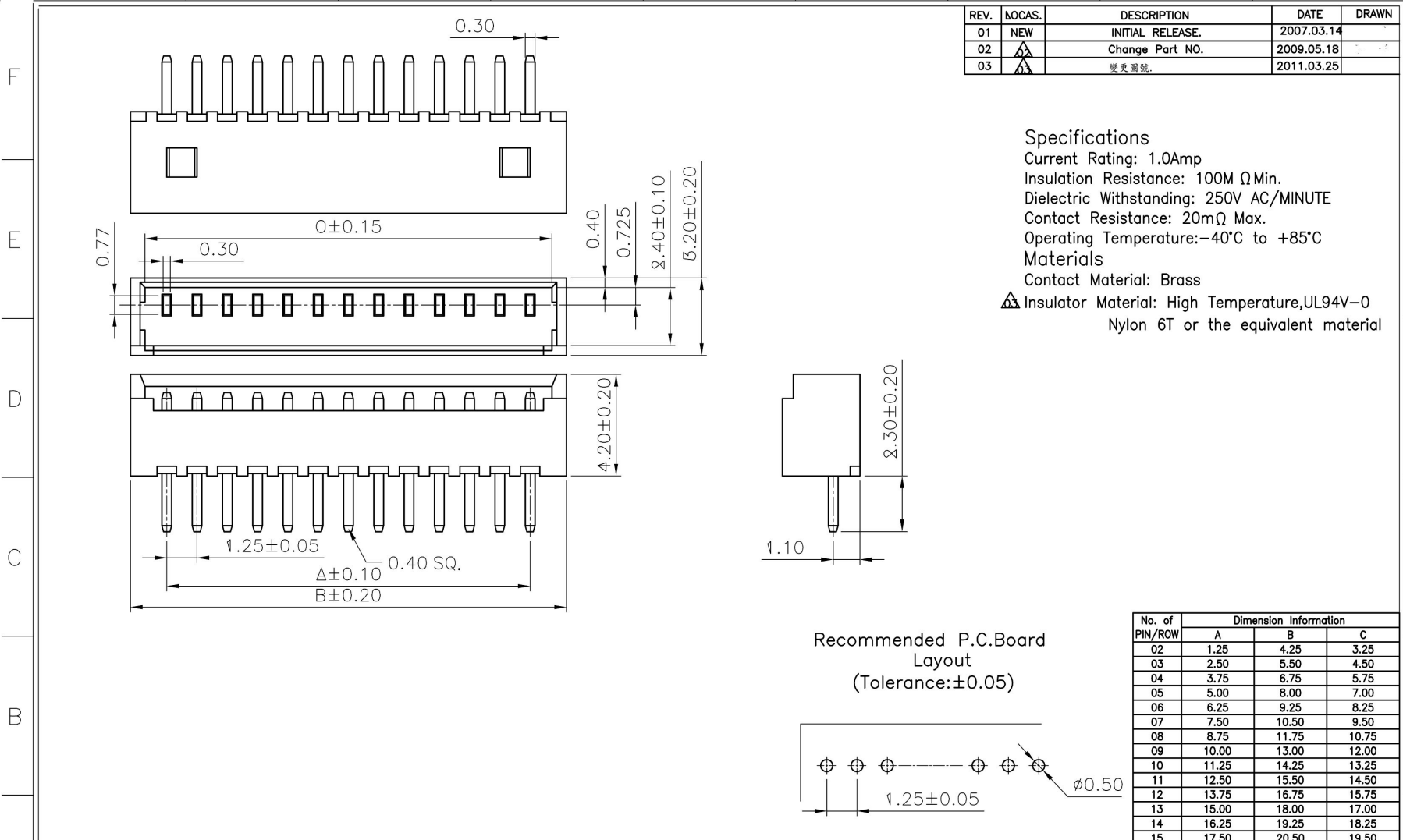


1 2 3 4 5 6 7 8 9

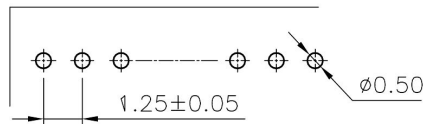


REV.	LOCAS.	DESCRIPTION	DATE	DRAWN
01	NEW	INITIAL RELEASE.	2007.03.14	
02	△△	Change Part NO.	2009.05.18	
03	△△	變更圖號.	2011.03.25	

Specifications
 Current Rating: 1.0Amp
 Insulation Resistance: 100M Ω Min.
 Dielectric Withstanding: 250V AC/MINUTE
 Contact Resistance: 20mΩ Max.
 Operating Temperature:-40°C to +85°C

Materials
 Contact Material: Brass
 △ Insulator Material: High Temperature,UL94V-0
 Nylon 6T or the equivalent material

Recommended P.C.Board
 Layout
 (Tolerance:±0.05)



No. of PIN/ROW	Dimension Information		
	A	B	C
02	1.25	4.25	3.25
03	2.50	5.50	4.50
04	3.75	6.75	5.75
05	5.00	8.00	7.00
06	6.25	9.25	8.25
07	7.50	10.50	9.50
08	8.75	11.75	10.75
09	10.00	13.00	12.00
10	11.25	14.25	13.25
11	12.50	15.50	14.50
12	13.75	16.75	15.75
13	15.00	18.00	17.00
14	16.25	19.25	18.25
15	17.50	20.50	19.50

HO CHIEN				GENERAL TOLERANCE		DESIGN:	DATE: 2011.03.25	DWG. NO.	TITLE:
				X.*±5.0*	X.±0.30				
REV. 03	UNIT. mm	SCALE. 5:1	PAGE. 1/1	.X*±3.0*	.X±0.25	AUDITED: Link	DATE: 2011.03.25	PART NO.	HCA9813-XX 1.25 180° WAFER
				.XX*±2.0*	.XX±0.15				
				.XXX*±1.0*	.XXX±0.10				

1 2 3 4 5 6 7 8 9